



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Kannan Raj et al.

Group Art Unit:

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09/839,023

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Filed:

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For:

Optically Interconnecting Multiple Processors

Atty. Dkt. No.:

ITL.0462US (P9816)

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Applicant submits the references listed on the attached form PTO 1449, copies of which are enclosed. A copy of a communication dated September 24, 2003 from a foreign patent office in a counterpart application is also enclosed. I, the undersigned, hereby certify that each item of information contained in the Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the Information Disclosure Statement. Please apply any charges or credits to Deposit Account No. 20-1504 (ITL.0462US).

Respectfully submitted.

September 30, 2003

Date

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Date of Deposit: September 30, 2003 I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated above s addressed to the Commissioner for Patents, P.O. Box 1450,

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Monica Jacobs

INDERMATION DISCLOSURE CITATION (Use several sheets if necessary)

of this form with next communication to applicant.

PTO-1449

ATTY DOCKET NO. SERIAL NO. ITL. Q462US (P9816) 09/839,023 APPLICANT(S): Kannan Raj et al.

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